

Amendments to the Specification:

Please replace the title on page 1, line 3 as follows:

SOLID ELECTROLYTIC CAPACITOR WITH FIRST AND SECOND ANODE WIRES

Please replace the paragraph beginning on page 17, line 26 with the following replacement paragraph:

The electric circuit shown in Fig. 4 is made up of a circuit 7, a power supply 8 and a solid electrolytic capacitor A1. The circuit 7 is the target for noise cancellation and power supply by the solid electrolytic capacitor A1. The circuit 7 may include a CPU, IC or HDD, for example. The solid electrolytic capacitor A1 is connected between the circuit 7 and the power supply 8 and utilized for preventing unnecessary noises generated from the circuit 7 from leaking toward the power supply 8 and for assisting power supply to the circuit 7. In this figure, R_{10a} and R_{10b} represent the resistances of the first and the second anode wires 10a and 10b, respectively, whereas L_{10a} and L_{10b} represent the inductances of the first and the second anode wires 10a and 10b, respectively. R_{22} and L_{22} respectively represent the resistance and inductance of the metal cover 22. R_2 and L_2 represent the combined resistance and inductance, respectively, of the external anode terminal 21 and conductive member 26. R_{31} and L_{31} represent the combined resistance and inductance, respectively, of the external cathode terminal 31 and conductive resin layer 35. As shown in Fig. 1, as the current paths between the porous sintered body 1 and the external anode terminal 21, a path of current which flows through the first anode terminal 11a and a path of current which flows through the second anode terminal 11b via the metal cover 22 exist. As shown in Fig. 4, in the cancellation of noises generated from the circuit 7, the noises are distributed from the external anode terminal 21 to the two current paths and then flow into the porous sintered body 1. When the capacitor is used for power supply, the electric energy stored in the solid electrolytic capacitor A1 is distributed to the two current paths and then discharged from the external anode terminal 21.

Please replace the paragraph beginning on page 26, line 7 with the following replacement paragraph:

The solid electrolytic capacitor A4 shown in Figs. 14-16 includes a metal cover 32 electrically connected to the solid electrolytic layer of the porous sintered body 1 and having a plurality of holes 32c. The metal cover 32 accommodates the porous sintered body 1 and is bonded to the porous sintered body 1 via a conductive resin layer 35, as shown in Figs. 14 and 15. As shown in Figs. 15 and 16, opposite ends of the metal cover 32 provide external cathode terminals 32a. As shown in Figs. 14 and 15, an anode metal plate 23 is laminated to the lower surface of the porous sintered body 1 via a resin film 52. As shown in Figs. 14 and 16, an end of the anode metal plate 23 provides an external anode terminal 23a.